

AMENDMENT TO SPECIFICATION

Please replace the paragraph being on page 1, line 1 with the following amended paragraph:

BACKGROUND

1. FIELD OF INVENTION

The present invention relates to a circuit arrangement for generating a sawtooth current in a cathode ray tube deflection coil.

Please replace the paragraph being on page 1, line 8 with the following amended paragraph:

2. DESCRIPTION OF RELATED ART

Such a circuit arrangement, display device, and television apparatus are known from US 3,906,305. The known circuit arrangement includes a first and a second multiresonant section arranged in series. Each multiresonant section comprises a trace capacitor, a retrace capacitor, and a coil. One of the coils in the two or more sections is a line deflection coil of a cathode ray tube (CRT). The second section comprises an inductor in the form of a coil. Simultaneously, each section provides a common trace or first resonant period, in which the sawtooth current is generated in the CRT deflection coil and a similar current is generated in the coil-type inductor. The trace period is followed by a retrace period in each of the sections, in which the currents go back to their original value in a very short second resonant period of time. Semiconductor blocking means are connected in parallel to each of the sections and to the series arrangement of sections to control these trace and retrace periods. The circuit arrangement further has a driver modulator that is coupled in parallel to the trace capacitor of the second multiresonant section in order to provide the well known east- west raster correction of the CRT line deflection current. The inventor of the present invention has found that it is a disadvantage of the known circuit arrangement that it comprises many components which, apart from being relatively expensive and space consuming, also show a considerable power dissipation, which in general makes them more susceptible to breakdown.

Please replace the paragraph being on page 2, line 4 with the following amended paragraph:

SUMMARY OF INVENTION

According to the invention, a circuit arrangement is for this purpose provided for generating a sawtooth current in a cathode ray tube deflection coil, the circuit arrangement comprising a first and a second multiresonant section arranged in series, the cathode ray tube deflection coil being comprised in the first section, and the second section comprising an inductor, each section providing a common trace period and a common retrace period to provide a flyback of said sawtooth current, and the circuit arrangement comprising a current control circuit coupled to the inductor for controlling the course of the sawtooth current. In this respect, the course of the sawtooth current is its instantaneous value as a function of time. It is an advantage of the circuit arrangement according to the present invention that, although the known arrangement has been scrutinized since its first development many years ago, the current control now devised makes the voluminous and expensive trace capacitor in the second multi-resonant section superfluous. However, also the second multiresonant section remains capable of acting properly in a kind of multi-resonant mode, both during the trace and during the retrace period.

Please replace the paragraph being on page 3, line 9 with the following amended paragraph:

BRIEF DESCRIPTION OF DRAWINGS

The circuit arrangement according to the invention will now be elucidated further together with its additional advantages, while reference is being made to the appended drawing. In the drawing:

Please replace the paragraph being on page 3, line 18 with the following amended paragraph:

DETAILED DESCRIPTION

Fig. 1 shows a basic circuit diagram of a circuit arrangement 1. Such a circuit arrangement 1 forms part of a circuit for generating a line current in a line deflection coil L1 in a display device such as, for example, a monitor or a television. The arrangement 1 also comprises a line transformer only partly shown in Fig. 1 and referenced L3. Further details, as regards additionally effecting image corrections or the deriving of an extremely high voltage (EHT) by the line transformer from a line voltage at terminal A for the acceleration anode of a color CRT, are outlined in US 3,906,305, whose disclosure is included herein by reference.